



CHHATTISGARH SWAMI VIVEKANAND TECHNICAL UNIVERSITY, BHILAI  
छत्तीसगढ़ स्वामी विवेकानंद तकनीकी विश्वविद्यालय, भिलाई

No.: CSVTU/Ph.D./2026/ 1558

Bhilai-Newai, Date: 7/4/26

**NOTICE**

**Ph.D. VIVA-VOCE EXAMINATION (OPEN DEFENCE)**

It is hereby notified that the open defence of Ph.D. is scheduled to be held in the university as per the details given below:-

Sl. No.	Particulars	Description
1	Name of the Research Scholar	RITU SINGH
2	Branch	Electrical Engineering
3	Faculty	Faculty of Electrical, Electronics Telecommunication Engineering and Instrumentation
4	Enrollment No.	BC 7420
5	Supervisor	Dr. S.P.Shukla, Professor, BIT, Durg (C.G.)
6	Research Centre	Bhilai Institute of Technology, Durg (C.G.)
7	Research Title	"INTEGRATION OF HYBRID SYSTEM TO MICROGRID THROUGH IMPROVED POWER ELECTRONICS INTERFACES"
8	Scheduled Date & Time of Viva-Voce	15 <sup>th</sup> April 2026 (Wednesday), 12.00 Noon.
9	Venue	CSVTU, Bhilai, Conference Hall
10	Mode of Viva-Voce	Online

All subject related/ interested faculty members, research scholars and students are cordially invited to attend the Ph.D. (open defence) viva-voce examination.

Exam Controller  
CSVTU, Bhilai (C.G.)

Endt. No: CSVTU/Ph.D./2026/ 1559

Bhilai-Newai, Date: 7/4/26

**Copy to:**

1. PS to VC (for kind information).
2. Registrar, CSVTU Bhilai.
3. OSD-Acad, CSVTU, Bhilai.
4. OSD (Info) for information and uploading the notice in the university website.
5. CFO, CSVTU Bhilai (CG).
6. Dr. S.P.Shukla, Professor, BIT, Durg (C.G.)
7. Dean (Dr.(Mrs.) Anupama Pushkar Huddar Professor, Electrical Engineering, BIT, Durg) (C.G.)
8. Chairman BoS (Dr. S.P.Shukla, Professor, BIT, Durg (C.G.)) (C.G.)
9. Chairman DRC/Principal BIT, Bhilai (C.G.)
10. Principal All approved research centre, CSVTU, Bhilai.
11. All Concerned faculty members of the affiliated institutions.
12. I/C Video Conferencing for needful preparation.
13. Notice Board.

Exam Controller  
CSVTU, Bhilai (C.G.)

Newai, P.O. Newai, Bhilai, Distt. Durg (Chhattisgarh) - 491107

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